If you are using a printed copy of this procedure, and not the on-screen version, then you <u>MUST</u> make sure the dates at the bottom of the printed copy and the on-screen version match.

The on-screen version of the Collider-Accelerator Department Procedure is the Official Version.

Hard copies of all signed, official, C-A Operating Procedures are kept on file in the C-A ESHQ

Training Office, Bldg. 911A.

C-A OPERATIONS PROCEDURES MANUAL

ATTACHMENT

4.120.52.b U-Upstream & V-Target (PEER 23) Gate Tests

C-A-OP	M Procedures in w	which this Attachmen	t is used.
4.120.52			
	Hand Proce	essed Changes	
HPC No.	<u>Date</u>	Page Nos.	<u>Initials</u>
		Signature on F	
	Collide	er-Accelerator Depar	tment Chairman

V. Castillo

4.120.52.b U-Upstream & V-Target (PEER 23) Gate Tests

PASS SEMI-ANNUAL ACCEPTANCE TEST PROTOCOL

Division A Software Filename and Checksum: Title:	Checksum:
Division B Software Filename and Checksum: Title:	Checksum:
<u>Initial testing complete</u> :	
Test Team Leader's Name (Print):	Life Number:
Test Team Leader's Name (Sign):	Date:/
Acceptance test procedure complete (following repairs and retesting if required):	
Test Team Leader's Name (Print):	Life Number:
Test Team Leader's Name (Sign):	Date://
Test results reviewed by:	
Safety Section Head's Name (Print):	Life Number:
Safety Section Head's Name (Sign):	Date:/
Test results accepted by Radiation Safety Committee:	
RSC Member's Name (Print):	Life Number:
RSC Member's Name (Sign):	Date:/

2

1.1 CONDUCT Visual check on Peer 23 gates following Table-1, below

	Micro	Switch	Elec	Gate		Gate F	unctions		Verify	Inspn
Gate	Align	Opern	Wiring	Box	Lights	Open	Self-	Latch	all x's	O.K.
							Closing		Corr.	Init.
UGE1										
UGI1										
VTGE										

Legend: Tick = O.K. x = Problem N/A = Not Applicable

Table 1: Summary of Physical Inspection of Peer 23 Gates

1.2	Test of Entry	GATE at UGE1		
		VERIFY	Entry Gate at UGE1 has been inspected	
		STATION	One Personnel inside gate	
		PLACE	PEER 23 in Controlled Access (MODE 16)	
		VERIFY	PEER 23 is in Controlled Access	MODE 16
		VERIFY	The Exterior gate box Controlled Access light is	ON
		OPEN	Gate UGE1 with Simultaneous Release and #6	
			CA Key	
		VERIFY	Simultaneous Release Buzzer	SOUNDS
		VERIFY	Gate UGE1 is	OPEN
		VERIFY	MCR sees the gate is	OPEN
		HOLD	The Electric Strike micro switch	MADE
		HOLD	Both of the gate micro switches	MADE
		VERIFY	MCR sees the gate is	NOT RESET
		RELEASE	Div A micro switch	
		VERIFY	MCR indicates	DIV $A \neq DIV B$
		HOLD	Both of the gate micro switches	MADE
		VERIFY	MCR sees the gate is	NOT RESET
		RELEASE	Div B micro switch	
		VERIFY	MCR indicates	DIV $A \neq DIV B$
		HOLD	Both of the gate micro switches	MADE
		VERIFY	MCR sees the gate is	NOT RESET
		RELEASE	The Electric Strike micro switch	
		VERIFY	MCR sees the gate is	OPEN
		CLOSE	The gate	
		VERIFY	MCR sees the gate is	NOT RESET
		ATTEMPT	Remote reset from MCR	
		VERIFY	MCR sees Div $A \square$ and Div $B \square$	RESET
		OPEN	Gate from inside	
		VERIFY	MCR sees Div A \square and Div B \square	NOT RESET
		CLOSE	The gate	
		PLACE	PEER 23 in Restricted Access (Mode 8)	
		VERIFY	PEER 23 is in Restricted Access	MODE 8
		VERIFY	The Exterior gate box Restricted Access light is	ON
		OPEN	Gate UGE1 from gate box with the Zero key	
		VERIFY	Gate UGE1 is	OPEN
		OPEN	Gate UGE1 with the #6 RC CA key	
		VERIFY	Gate UGE1 is	OPEN
		CLOSE	The gate	
			-	

PLACE	PEER 23 in Safe Access (Mode 2)	
VERIFY	PEER 23 is in Safe Access	MODE 2
VERIFY	The Exterior gate box Controlled Access light is	ON
OPEN	Gate UGE1 with Simultaneous Release and Zero	
	key	
VERIFY	Attempt to open gate UGE1 with Simultaneous	
	Release and Zero key	FAIL
OPEN	Gate UGE1 with Simultaneous Release and #6	
	CA Key	
VERIFY	Gate UGE1 is	OPEN
CLOSE	Gate UGE1	

☐ Check for acceptance of Test of Entry Gate at UGE1

1.3

Test of G	ATE at UGI1		
	VERIFY	Gate at UGI1 has been inspected	
	PLACE	PEER 23 in Controlled Access (MODE 16)	
	VERIFY	PEER 23 is in Controlled Access	MODE 16
	VERIFY	The gate box Controlled Access light is	ON
	OPEN	Gate UGI1 with Simultaneous Release and #6 CA	
		Key	
	VERIFY	Simultaneous Release Buzzer	SOUNDS
	VERIFY	Gate UGI1 is	OPEN
	VERIFY	MCR sees the gate is	OPEN
	HOLD	The Electric Strike micro switch	MADE
	HOLD	Both of the gate micro switches	MADE
	VERIFY	MCR sees the gate is	NOT RESET
	RELEASE	Div A micro switch	
	VERIFY	MCR indicates	DIV $A \neq DIV B$
	HOLD	Both of the gate micro switches	MADE
	VERIFY	MCR sees the gate is	NOT RESET
	RELEASE	Div B micro switch	
	VERIFY	MCR indicates	DIV $A \neq DIV B$
	HOLD	Both of the gate micro switches	MADE
	VERIFY	MCR sees the gate is	NOT RESET
	RELEASE	The Electric Strike micro switch	
	VERIFY	MCR sees the gate is	OPEN
	CLOSE	The gate with Personnel on U-Up side	
	VERIFY	MCR sees the gate is	NOT RESET
	VERIFY	The gate box Gate Reset light is	OFF
	RESET	The gate with #7 Sweep key at U-Up gate box	
	VERIFY	MCR sees the gate is	RESET
	VERIFY	The U-Up gate box Gate Reset light is	ON
	OPEN	The gate without Simultaneous Release	
	VERIFY	MCR sees the gate is	OPEN
	VERIFY	The U-Up gate box Gate Reset light is	OFF
	CLOSE	The gate	

PLACE	PEER 23 in Safe Access (Mode 2)	
VERIFY	PEER 23 is in Safe Access	MODE 2
VERIFY	The gate box Controlled Access light is	ON
OPEN	Gate UGI1 with Simultaneous Release and Zero	
	key	
VERIFY	Attempt to open gate UGI1 with Simultaneous	
	Release and Zero key	FAIL
OPEN	Gate UGI1 with Simultaneous Release and #6 CA	
	Key	
VERIFY	Gate UGI1 is	OPEN
CLOSE	Gate UGI1	

☐ Check for acceptance of Test of Gate at UGI1

1.4 Test of Entry GATE at VTGT VERIFY Ent

est of i	Entry GATE at		
	VERIFY	Entry Gate VTGE has been inspected	
	PLACE	PEER 23 in Controlled Access (MODE 16)	
	VERIFY	PEER 23 is in Controlled Access	MODE 16
	VERIFY	The Exterior gate box Controlled Access light is	ON
	OPEN	Gate VTGE with Simultaneous Release and #6	
		CA Key	
	VERIFY	Simultaneous Release Buzzer	SOUNDS
	VERIFY	Gate VTGE is	OPEN
	VERIFY	MCR sees the gate is	OPEN
	HOLD	The Electric Strike micro switch	MADE
	HOLD	Both of the gate micro switches	MADE
	VERIFY	MCR sees the gate is	NOT RESET
	RELEASE	Div A micro switch	
	VERIFY	MCR indicates	DIV $A \neq DIV B$
	HOLD	Both of the gate micro switches	MADE
	VERIFY	MCR sees the gate is	NOT RESET
	RELEASE	Div B micro switch	
	VERIFY	MCR indicates	DIV $A \neq DIV B$
	HOLD	Both of the gate micro switches	MADE
	VERIFY	MCR sees the gate is	NOT RESET
	RELEASE	The Electric Strike micro switch	
	VERIFY	MCR sees the gate is	OPEN
	CLOSE	The gate	
	VERIFY	MCR sees the gate is	NOT RESET
	RESET	The gate with #7 Sweep key	
	VERIFY	MCR sees Div A \square and Div B \square	RESET
	OPEN	Gate with Simultaneous Release	
	ABORT	Simultaneous Release with gate open	
	VERIFY	\mathbf{MCR} sees Div $\mathbf{A} \square$ and Div $\mathbf{B} \square$	NOT RESET
	CLOSE	The gate	

		PLACE	PEER 23 in Restricted Access (Mode 8)	
		VERIFY	PEER 23 is in Restricted Access	MODE 8
		VERIFY	The Exterior gate box Restricted Access light is	ON
		OPEN	Gate VTGE from gate box with the Zero key	
		VERIFY	Gate VTGE is	OPEN
		OPEN	Gate VTGE with the #6 RC CA key	
		VERIFY	Gate VTGE is	OPEN
		CLOSE	The gate	
		PLACE	PEER 23 in Safe Access (Mode 2)	
		VERIFY	PEER 23 is in Safe Access	MODE 2
		VERIFY	The Exterior gate box Controlled Access light is	ON
		OPEN	Gate VTGE with Simultaneous Release and Zero	
			key	
		VERIFY	Attempt to open gate VTGE with Simultaneous	
			Release and Zero key	FAIL
		OPEN	Gate VTGE with Simultaneous Release and #6	
			CA Key	
		VERIFY	Gate VTGE is	OPEN
		CLOSE	Gate VTGE	
			END OF TEST PROCEDURE	
TTL: Sign for co	omplet	tion of initial tes	ting:	
			Date: /_	/
TTL: Sign for co	omplet	tion of final testi	ng:	
	F-24			
			Date: /_	/

6